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**The Omega Lab**

Contents

[Welcome 3](#_Toc41312730)

[Overview 3](#_Toc41312731)

[How Do Omega Labs Work? 3](#_Toc41312732)

[Switching Between Alpha and Omega Labs 4](#_Toc41312733)

[Are Omega Labs Right for You? 4](#_Toc41312734)

[Grading 5](#_Toc41312735)

[Deadlines and Deliverables 7](#_Toc41312736)

[Lab Requirements 9](#_Toc41312737)

[What to do in a Lab Session 9](#_Toc41312738)

[Project Plan 9](#_Toc41312739)

[Presentation 10](#_Toc41312740)

[Project Manual 11](#_Toc41312741)

[Proof of Concepts 12](#_Toc41312742)

[Concept List (Proof of Concepts) 12](#_Toc41312743)

[Building Blocks (Project Manual) 15](#_Toc41312744)

# Welcome

## Overview

Omega-labs are a design-based alternative to the traditional Alpha-labs, which are procedure-based. In most classes, labs are a set of instructions that you follow and report on with an academic journal style paper. But this isn’t what a real-world engineering job is like; most engineers need to come up with their own design and write user manuals and datasheets.

In Omega-Labs, you will have the freedom to design your own circuit based on what you have learned in the course. The requirements of the reports and evaluations of projects will be based on what is generally expected in a job.

Omega-Labs emphasize personal growth, learning from failure, planning, and iterative design. You will not be graded only on the overall functionality of the project. Instead, your grade will be based partially on your ability to meet your goals and incorporate feedback into your project and partially on the functionality of individual building blocks of the circuit and your ability to analyze them.

## How Do Omega Labs Work?

Omega Labs are broken into 3 Milestones, each of which corresponds to a set of concepts. During each Milestone, you will be expected to design a circuit that contains **at least 1 circuit building block for MS1 and at least 2 circuit building blocks for MS2** **and MS3** from the corresponding course material, and that you will apply the course concepts to analyze your circuit. A building block might be a voltage divider, an amplifier, or a filter. A list of building blocks will be shown later in this document.

In addition to these building blocks, you can also include additional circuits not covered in this course such as digital IC’s, mechanical components, microcontrollers and more. If you don’t know any of the building blocks, you can still do omega-labs!

In fact, that’s where we expect you to start. We expect that your project will evolve throughout the semester as you learn more concepts and building blocks. We will be there to guide you through the process of project planning and making design decisions.

Deadlines for Omega-Labs are broken into 3 Milestones.

At each milestone you must provide:

1. Updated Project Plan
2. 5-Minute Informal Presentation
3. Omega Lab Individual Check-In
   1. 1-3 Minute Video
   2. Gradescope online questionnaire
4. Project Manual
5. Proof of Concepts (PoC)
6. LTSpice Circuit Simulation
7. Physical Circuit

Each of these will be explained further in the next sections.

## Switching Between Alpha and Omega Labs

**You don’t have to do Omega-Labs for the entire semester, in fact, you can go in and out of Omega-labs.** Because both Alpha- and Omega-labs are based on the Course Concepts, it’s easy to apply some concepts in your Omega-lab design and use alpha labs to ‘fill-out’ the others as needed. This can mean doing Omega-Labs for only part of the semester. For example, you may do Alpha-Labs for the first Milestones, and Omega-Labs for Milestones 2 and 3. **You can also do multiple small design projects instead of one big design project.**

## Are Omega Labs Right for You?

Unlike Alpha Labs, Omega Labs are not consistent, straightforward, and predictable in terms of difficulty and time. Omega-Labs are potentially unpredictable and require you to be more involved. However, Omega-Labs provide many things Alpha-Labs do not: **the freedom to build whatever you want and the chance to practice what engineers really do: *design***. To decide which Lab is best for you, think about your time constraints for the rest of the semester and what classes you’re taking. Consider too that you could use your Omega-Lab project in another class or independent study. Think about how you learn best as well - do you need a structured assignment to learn, or do you do best by diving in and creatively applying the concepts?

## Grading

You will not be graded only on the overall functionality of your entire circuit; If your whole circuit does not work, you may not lose any points from your lab grade. However, each individual building block must be functional on its own. One purpose of this design lab is to give you a safe space to try new things and fail. By not grading you on the functionality of the whole circuit, we hope to encourage exploration and to allow you to experience and learn from failed designs.

You will be graded based on your progress (have you met your goals, incorporated feedback, and do you know what needs to be done next), your technical accomplishments (functionality of building blocks, design decisions, communication in manual), and your Proof of Concepts. The grade distribution is meant to distribute the grade evenly between mathematical analysis, design, and progress. In addition, 40% of your lab grade depends on the completion of 1 Proof of Skills Check-in and 3 separate Omega Lab Individual Check-Ins that highlight your individual contribution to your labs. The Omega Lab Check-Ins will also include a peer evaluation of your group members.

**All of the Proof of Concepts together is worth 33% of the total lab grade.**

**TO OPT OUT OF THE FINAL, YOU MUST:**

1. Complete Milestone 3
2. Physically connect/integrate the Milestone 3 Circuit with either the Milestone 1 or 2 Circuit
3. Get at least an 80% on each and every assignment[[1]](#footnote-1) for Milestone 3 and one other Milestone
4. Complete all Individual Check-Ins with significant contributions to the lab group

**If you don’t meet the grade requirement, there will be an opportunity at the end of the semester to optimize your circuit and earn some points back.**

**Documentation Grade (9% of overall grade)**

|  |  |
| --- | --- |
| **Assignment** | **Grade Percent** |
| Proof of Concepts | 33.3% |
| Project Manual | 33.3% |
| Presentation | 33.3% |
| ProjectPlan | 1% |

**Individual Check-In Grade (6% of overall grade)**

|  |  |
| --- | --- |
| **Assignment** | **Grade Percent** |
| 1-3 minute video | 50% |
| Online Individual Check-In Submission | 50% |

## Deadlines and Deliverables

Omega-Labs are divided into 3 Milestones. A major part of the project is due at the end of each milestone, and minor sections are due halfway between each milestone. The dates listed are the on-time due dates. Please see Gradescope for late due dates.

|  |  |
| --- | --- |
| **Due Dates** | **Assignment** |
| **1/30** | **Proof of Skills (pre-lab assessment)** |
| **2/1** | **Proof of Skills Check-In (pre-lab check-in)** |
| 2/7 (Tuesday before Lab!) | Project Plan MS1 |
| 2/22 *(any document can be submitted as early as 2/15/23…due dates are extended due to Exam 1)* | Milestone 1 Checkoff:    Individual Check-In Video *(post in WebEx Teams YOUR team’s Lab space)*  Online Gradescope Individual Check-In *(Gradescope)*  Proof of Concepts 1 *(Gradescope)*    Project Presentation *(post in WebEx Teams YOUR team’s Lab space)*    LTSpice Schematic *(post in WebEx Teams YOUR team’s Lab space)* |
| 2/28 (Tuesday before Lab!) | Project Manual MS1  Project Plan MS2 |
| 3/29 | Milestone 2 Checkoff:  Individual Check-In Video  Online Gradescope Individual Check-In  Proof of Concepts 2    Project Presentation    LTSpice Schematic |
| 4/4 (Tuesday before Lab!) | Project Manual MS2  Project Plan MS3 |
| 4/26  ***WARNING! THIS IS YOUR LAST WEEK!! PLAN AHEAD!!!!*** | Milestone 3 Checkoff:  Individual Check-In Video  Online Gradescope Individual Check-In    Proof of Concepts 3    Project Presentation    LTSpice Schematic |
| 4/26 | All Optimizations from MS1 and MS2 Labs |
| 4/29 **(remember you can submit to the extended late due…see Gradescope)** | Project Manual MS3 |

# Lab Requirements

The following is a short description of the assignments you must complete at each Milestone. Make sure you use the corresponding assignment template to find more detailed information and to complete each assignment.

## What to do in a Lab Session

Omega Labs are structured so that you should be able to complete 1 concept for your PoC every week. The intention is that the lab sessions will go as follows:

1. Choose a building block you want to design.
2. Determine what equation governs that building block and what Concept it corresponds to.
3. Decide how you want the circuit to function and analyze the circuit using that Concept to choose component values.
4. Create the circuit in LTSpice and confirm that those component values allow the circuit to operate the way you intended. If not, check your simulation or go back to step 3.
5. Implement a physical version of the circuit and confirm that the circuit operates as predicted by your calculations and simulation. If not, check your wiring or research non-idealities that may cause the circuit to operate differently.
6. Use the prior steps to fill out your PoC.

This workflow is intended to help you through the design process for creating your building blocks. The PoC’s and the process you went through to create them will then help you complete your Project Manual.

### Project Plan

The Project Plan is a sheet that is meant to help you plan out your project, and it helps both you and the TA make sure that project scope is reasonable. The Plan asks you to outline what your project is, why you want to do it, what circuits you will need to build, and what goals you want to achieve in each Milestone period. This plan must be approved by a TA before you start your project.

A Project Plan is due at the start of each Milestone Period for those just starting Omega-Labs, and it is due during the Project Presentation for those already in Omega-Labs. The Project Plan is not graded directly but may be graded indirectly during the Project Presentation under the Presentation Grade’s ‘Planning’ Category.

### Presentation

This is an informal presentation to the TA and Professor (via Demonstration Video) to demonstrate the functionality of your circuit and discuss progress. In this presentation, you should:

* Explain the purpose of the circuit and its high-level operation.
* Present a functional LTSpice simulation
* Demonstrate functionality of circuit (If the overall circuit doesn’t work, you MUST demonstrate functionality of individual building blocks)
* Explain circuit operation
* Discuss problems encountered and solutions
* Support design choices
* Discuss plans for next Milestone

You can include anything else you deem necessary. The Milestone presentation is a good opportunity to get feedback and ask for advice. Plan for this to take ~5-10 minutes.

You will be graded on the following standards:

**Presentation Standards**

1. **I can explain the goal of the project and its scope within the course.**
2. **I can present a high-level block diagram that represents the functional blocks of each part of my demonstration.**
3. **I can show calculations and, if needed, reasonable assumptions that helped me predict the correct function of my circuit.**
4. **I can show my simulated circuit and show important probe points to compare to my mathematic predictions**
5. **I can demonstrate the course concept as a working functional block or working analysis in my circuit or experimental outcome.**
6. **I can show important functional blocks that work as expected OR attempt to explain why it failed through troubleshooting.**
7. **I can discussion design choices directly related to concepts I’m learning in Electric Circuits**
8. **I can discuss ideas OR design choices or ideas that are beyond the content of Electric Circuits.**
9. **I can discuss plans for the next lab.**
10. **I can articulate at least ONE question based on my experience doing the Omega Labs or I can answer a question about my Omega Lab.**

You are expected to have all building blocks fully integrated by the presentation. If the overall circuit is not functional then at least the individual building blocks should be functional, and you should provide some **technical** reason as to why integration of the building blocks was challenging.

### Project Manual

The Project Manual should include the following sections. Please see the Project Manual assignment for more details, a template, and an example document.

1. Description
   1. Complete schematic and block diagram
   2. High-Level Description of the operation and intended application
   3. Description of related pre-existing applications.
2. Operation and Design
   1. Schematic of building block
   2. Design Equations (equation relating input and output of building block)
   3. Plot of input and output of each building block **in isolation**
   4. Discussion of design choices (why you chose specific component values for each building block).
3. Integration and Optimization
   1. Describe the overall integration of the building blocks into one circuit
   2. Explain how you designed each building block so they could be connected together (support design choices).
   3. Reference the building block design equations to explain how each building block was designed and how they work together.
   4. Discuss any issues you had in getting building blocks to work together.
   5. Plot the overall input and output of the integrated circuit and any other measurements that would be helpful in explaining operation.
4. Operating Conditions
   1. Describe limitations of the circuit including situations where it does not work
   2. Describe any tradeoffs present in the design
   3. Describe how the circuit could be improved

An engineer reading the project manual should be able to use it to rebuild and redesign the circuit. It might be a good idea to have a friend read your report first and see if they understand how the circuit works. Refer to Project Manual for more details on what to include in your project manual.

You will make a single Project Manual document over the entire semester. You will continually update the document based on feedback and updates to your project. Expect the first iteration to be about 10pgs.

### Proof of Concepts

See the Proof of Concepts assignment for more details, an assignment template, and examples.

The Proof of Concepts demonstrates that you can apply the mathematical concepts you are learning in the course to your circuit. The Proof of Concepts is meant to be a substitute for the work done in Alpha Labs, however the difference is that you will decide what to analyze based on what you are building. The Proof of Concepts should represent the analysis you’ve done to design your circuit. In each PoC submission, you must analyze a total of 4 of the listed Milestone concepts. For each concept, you should include:

* Header with concept name
* The name of the building block to which it corresponds
* A labelled circuit diagram
* A 1-2 sentence description of how you are applying the concept and what circuit variables you are analyzing to demonstrate it
* A mathematical analysis
* A simulation and plot
* A measurement and plot
* A brief discussion of the results
* An explanation of how this analysis helped in circuit design

See the example entries in the Proof of Concepts assignment document. Keep in mind that these entries do not need to be long - in fact, keep them as brief as possible! **You can apply multiple concepts to the same circuit**. Additionally, you can apply concepts to a circuit not in your final project, say a discarded design, so long as you support that the analysis of that circuit helped you to arrive at your final design.

### Concept List (Proof of Concepts)

For each PoC, choose 4 of the listed Milestone concepts to analyze. The pace to complete these PoC is ~1 per week. You may need to do more than one per week on occasion. DO NOT FALL BEHIND. Do not procrastinate.

**Milestone 1**

1. Ohm’s Law
2. Polarity
3. KVL
4. KCL
5. Nodal Analysis
6. Mesh Analysis
7. Circuit Reduction of Parallel and Series Resistors
8. Voltage Divider
9. Equivalent Sources
10. Superposition
11. Thevenin or Norton Circuit
12. Operational Amplifier as a Comparator
13. Operational Amplifier as an Amplifier

**Milestone 2**

1. Equivalent Impedance
2. Continuity Conditions
3. Time Constant
4. First-Order Circuit (RC or RL) with Differential Equations

* Must Analyze: Step Response with Differential Equations: Provide differential equation and time-domain function f(t) where f is the variable of interest

1. First-Order Circuit (RC or RL) with s-Domain Analysis

* Must Analyze: Step Response with Laplace (s-Domain): provide s-domain circuit, s-domain function F(s), time-domain function f(t)

1. Second-Order Circuit with Differential Equations

* Must Analyze: Step Response with Differential Equations: Provide differential equation and time-domain function f(t) where f is the variable of interest

1. Second-Order Circuit with s-Domain

* Must Analyze: Step Response with Laplace (s-Domain): provide s-domain circuit, s-domain function F(s), time-domain function f(t)

1. Op Amp with Capacitive/Inductive Feedback Network
2. Thevenin or Norton analysis applied to a First- or Second-Order Circuit

* Use Differential Equations OR s-Domain to analyze the step response

**Milestone 3**

1. Phasors
2. Complex Power
3. Transformers
   1. Ideal Transformer
   2. Real Transformer
4. First Order Active OR Passive Filter
   1. Must Analyze:
      1. Transfer Function
      2. Bode Plot
      3. Poles and Zeroes
      4. Cutoff Frequency
      5. Rolloff in dB
5. Second Order Active OR Passive Filter
   1. Must Analyze:
      1. Transfer Function
      2. Bode Plot
      3. Poles and Zeroes
      4. Resonant/Cutoff Frequency/Frequencies
      5. Rolloff in dB

## Building Blocks (Project Manual)

This is a list of potential building blocks for your project. This list is not comprehensive, and you can get new blocks approved by a TA or Professor in your Project Plan. Each building block can only satisfy a single unit requirement. This summer due to a shortened semester Milestones 1 and 3 require: An **Input** stage, ONLY 1 **Primary** stage block for the corresponding Milestone, and an **Output** stage. (You must choose a filter application for Milestone 3.) Milestone 2 **will require 2 Primary stage** building blocks. The Input and Output stages can remain the same throughout the semester.

**It is strongly recommended that you use the Op Amp OP482 or OP484 when constructing physical circuits and Universal Op Amp 2 when simulating circuits.**

If you want to understand more details on how to read a data sheet for OpAmps and more about OP484 specifically, learn from a professional and RPI alum, Doug Mercer!:

<https://sites.ecse.rpi.edu/~ssawyer/GeneralDesign/DougMercer_HowtoReadDatasheet_22322/DougMercer_HowtoReadDatasheet_22322.html>

**Input Stage (At Least 1)**

* Sensor
  + IR LED
  + Antennae
  + Photodetector
  + Microphone
  + Temperature Sensor
  + CO Gas Sensor
  + Methane Gas Sensor
  + Flex Sensor
  + Humidity Sensor
  + Piezoelectric Sensor
  + Many others!
* User Interface
  + Buttons
  + Switches
  + Potentiometer
* Power Generator
  + Solar Panel
  + Wind Turbine

**Milestone 1 (At Least 1)**

* Digital to Analog Converter (See note below)
* Inverting Amplifier
* Non-Inverting Amplifier
* Summer
* Difference
* Comparator
* Wheatstone Bridge
* Transimpedance Amplifier
* Schmitt Trigger
* Window Comparator

**Milestone 2 (At Least 2)**

* Analog-to-Digital Converter (See note below)
* Monostable Multivibrator (Timed Pulse Generator)[[2]](#footnote-2)
* Astable Multivibrator (Square Wave Generator)2
* Differentiator
* Integrator
* First-Order Passive Filter
* Second-Order Passive Filter
* Phase Shifter
* Sample and Hold Amplifier

**Milestone 3 (At Least 2 must choose filter)**

* Transformer
* First-Order Filter
* First-Order Passive Filter
* Second-Order Passive Filter[[3]](#footnote-3)
* First-Order Active Filter
* Second-Order Active Filter
* Transmission Line[[4]](#footnote-4)
* Oscillator Circuit (Digital to Analog Converter) (See Note below)

**Output Stage (At Least 1)**

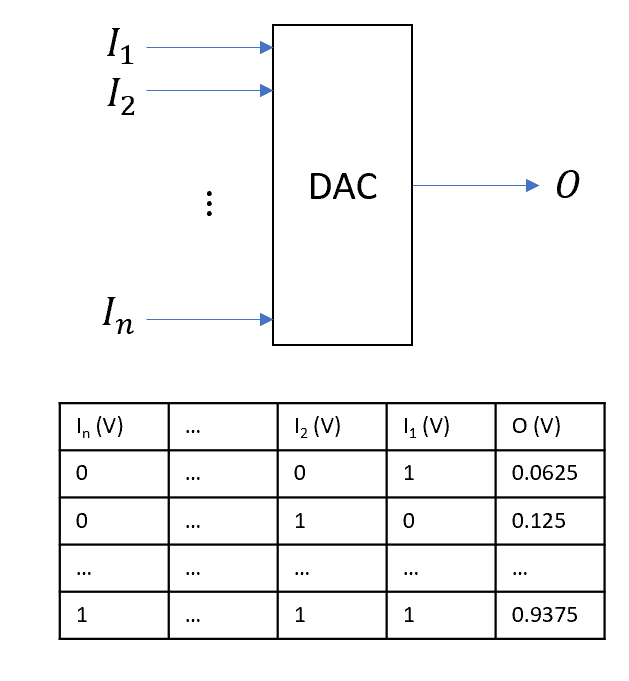
* Solenoids
* LED
* Display
* DC Motor
* Stepper Motor
* Buzzer
* Antennae

**Data can be an acceptable output if you are using statistical methods to analyze it. For example, you might make a sensor circuit to collect environmental data. You must get this confirmed by a TA first.**

**Additionally, you may use transistors in the circuit for powering high current devices. Other uses must be approved by the TA or Professor first.**

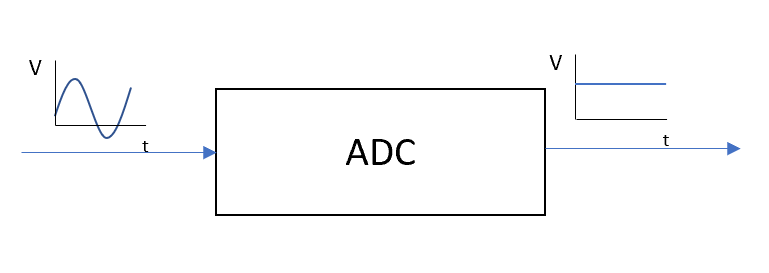
**Digital to Analog Converter MS1**

In MS1, a digital to analog converter means a circuit which converts n input values of two distinct DC voltages into a single DC voltage consisting of a range of possible values. One possible example is given below.



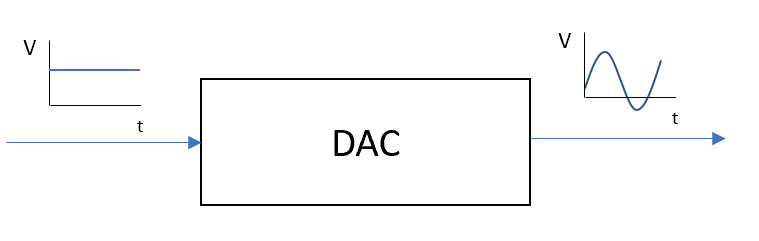
**Analog to Digital Converter**

In this course, analog to digital conversion means the conversion of a periodic waveform like a sine wave into a DC value.



**Digital to Analog Converter MS3**

In MS3, a digital to analog converter means the conversion of a DC value into a sinusoidal AC signal. This can be done in many different ways! Make sure you discuss with your TA if the DAC circuit you plan to use is appropriate and aligns with the course concepts.



1. There are 2 PoC assignments for each milestone. The average of these two must be at least 80%. [↑](#footnote-ref-1)
2. Do NOT use transistors to implement this circuit. Use an Op Amp or 555 Timer. [↑](#footnote-ref-2)
3. Two sequential first-order filters to form a second-order filter counts ONLY as a second-order filter building block. [↑](#footnote-ref-3)
4. If your circuit has a T-Line [↑](#footnote-ref-4)